

Practice&Innovation Track



Solving the Instance Model View-Update Problem in AADL

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Tool Demonstration Track OSATE-DIM Solves the Instance Model View-Update Problem in AADL

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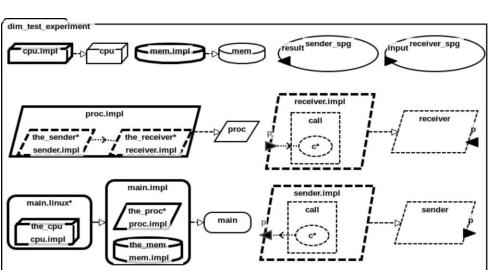
AADL

- Architecture Analysis and Design Language • SAE Standard AS5506D
- to model real-time embedded systems composed of
 - $\circ \quad \text{software and} \quad$
 - physical execution platform components
 - tightly coupled with actuators and sensors
 - to interact with their environments (Cyber-Physical Systems!)
- scheduling/flow-control analyses
- code generation for embedded platforms
- Adopted as the core of the US DoD Digital Engineering Strategy
 - o entering production within the Future Vertical Lift program of US Army
 - this project funded by US Army CCDC-DevCom ATLANTIC





AADL Constructs



public with Base Types; renames Base Types::all; with RAMSES Properties; system main 69 end main: **9**e system implementation main.impl 10 subcomponents 11 the proc: process proc.impl; 12 the mem: memory mem.impl; 13 properties 14 actual memory binding => (reference (the mem)) applies to the proc; 15 end main.impl; 16 **17**Θ system implementation main.linux extends main.impl 18 subcomponents 19 the cpu: processor cpu.impl {RAMSES Properties::Target => "linux";}; 20 properties 21 actual processor binding => (reference (the cpu)) applies to the proc; 22 end main.linux; 23 249 processor cpu 25 end cpu; 26 279 processor implementation cpu.impl 28

properties

package dim test experiment

- Scheduling Protocol => (RMS) ; end cpu.impl;
- 32⊝ process proc
- 33 end proc:

- Components ٠
 - Classifiers \cap
 - Type
 - Implementation
 - Extensions 0

- Features
 - Refinements Ο
- Connections

29

30

31

Refinements Ο



MODELS 2022 (Montreal, Canada)

Blended syntax

OSATE

- Open-Source AADL Tool Environment
- Eclipse IDE plugin
- Latest version (OSATE 2.11) compatible with Eclipse 2022-03
- Primarily maintained by SEI-CMU
- Safety-critical analyses and verification with languages like Resolute and AGREE
- Code generation capabilities with RAMSES, OCARINA
- <u>https://osate.org</u>
- Many industrial projects > many projects to make OSATE more robust

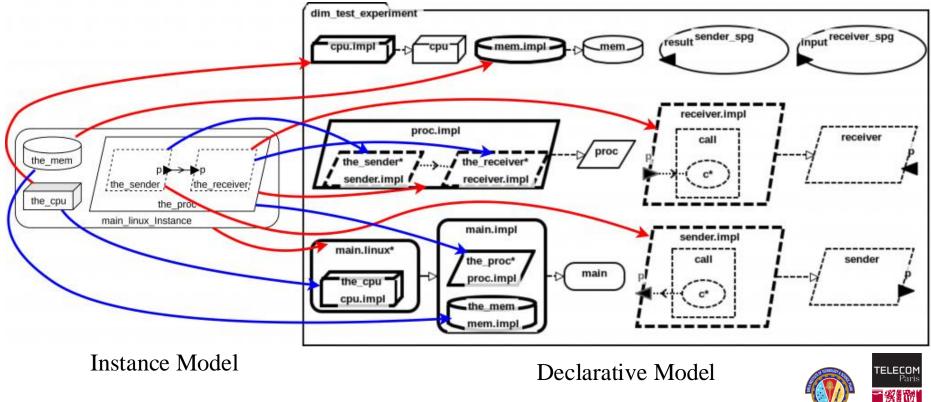




OSATE Instance Model

blue: subcomponent reference red: classifier reference

🚫 IP PARIS

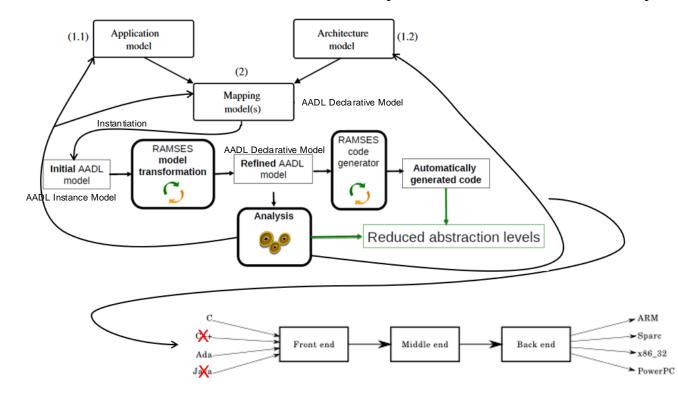


Why Instance model?

- Simplicity
- Tree-graph structure of containment
- No complex cross-tree relations (except property references)
 - No references up the branch as well
- All information stored locally within each component/element
- Hence, most tools use Instance models for analysis.

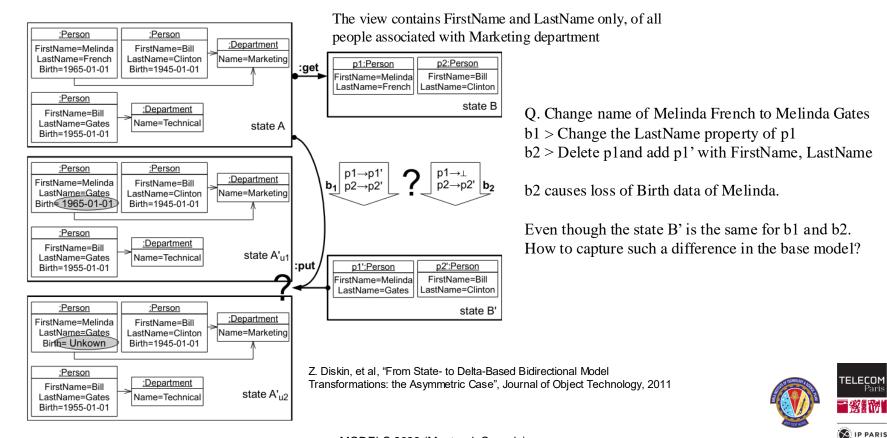


RAMSES Workflow (Refinement of AADL Models for the Synthesis of Embedded Systems)

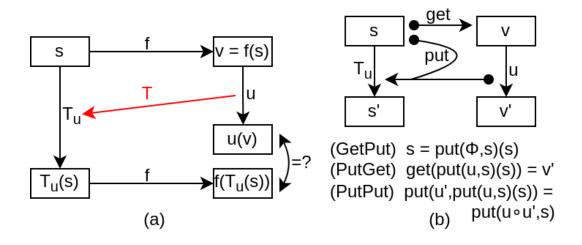




View-Update Problem



Solution: Delta-based Lens



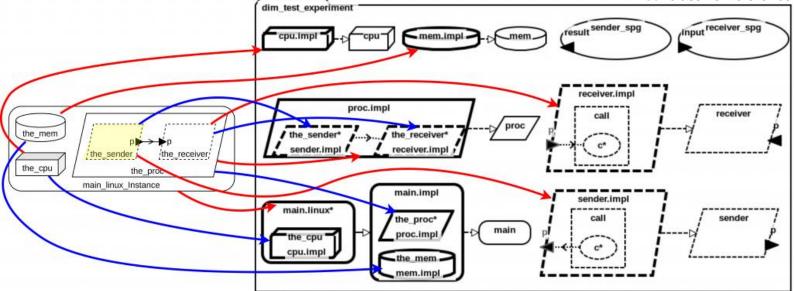
- s : Model State f : View-Generating Function v : View-State u : View-Update
- T : Translation

- => Declarative Model
- => Instantiation
- => Instance Model
- => Updates of Instance Model
- => Deinstantiation

f is not a bijection!



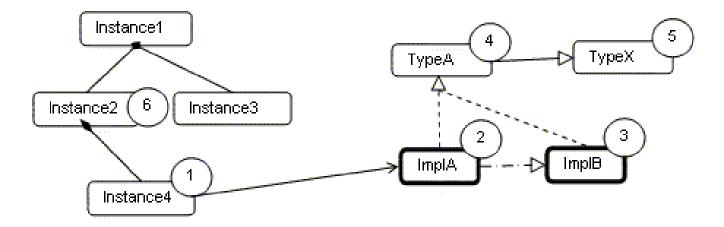
blue: subcomponent reference red: classifier reference



Q. Add a property to *the_sender*

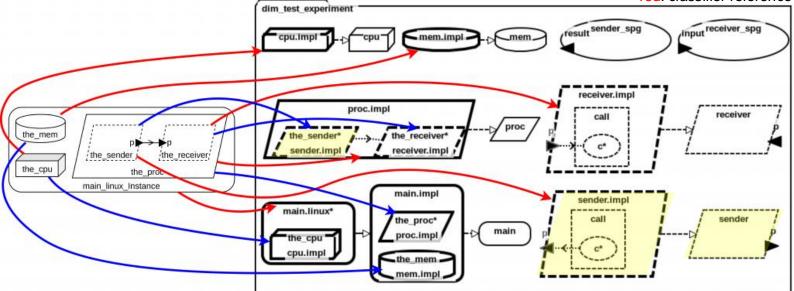


AADL Property Value Determination





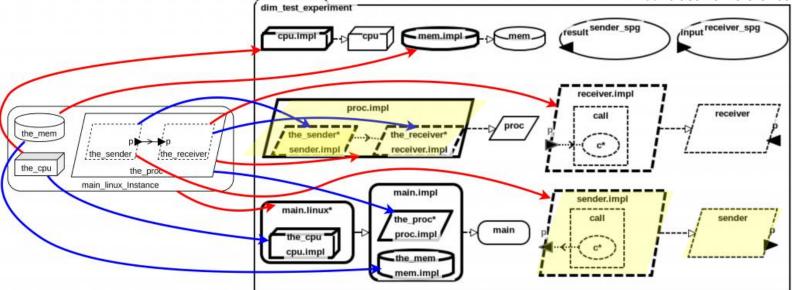
blue: subcomponent reference red: classifier reference



Q. Add a property to *the_sender* Soln. Add property to *the_sender** or *sender.impl* or *sender* ??



blue: subcomponent reference red: classifier reference



Q. Add a property to *the_sender* Soln. Add property to *the_sender** or *sender.impl* or *sender* or to *proc.impl* (*applies to the_sender**) ??



Too many choices for de-instantiation in AADL!



Another Example: Need for automated de-instantiation

```
system main
end main;
system implementation main.impl
    subcomponents
        proc1: process proc.impl;
        proc2: process proc.impl;
end main.impl;
process proc
end proc;
process implementation proc.impl
    subcomponents
        the sender: thread sender;
        the receiver: thread receiver;
    connections
        cnx: feature the sender.p -> the receiver.p;
end proc.impl;
```

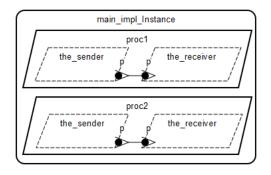
thread sender

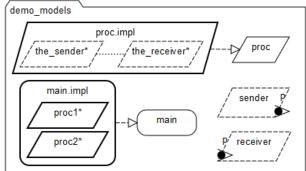
package demo models

public

```
features
    p: out feature;
end sender;
```

thread receiver features p: in feature; end receiver; end demo_models;

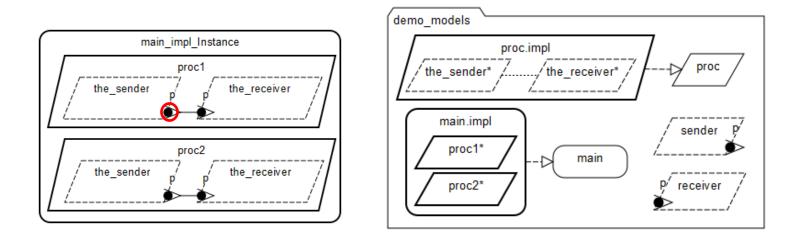




proc1 and proc2 have the same classifier proc.impl



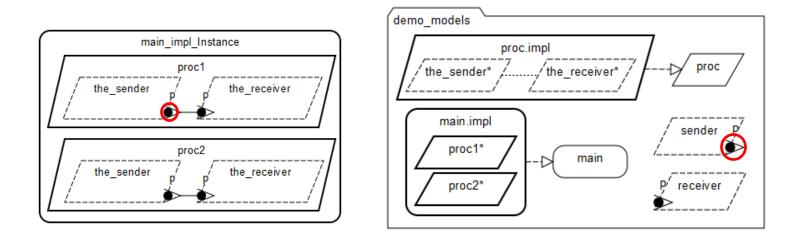
Another Example: Need for automated de-instantiation



We want to change the abstract feature *p* in *proc1.the_sender* to data port



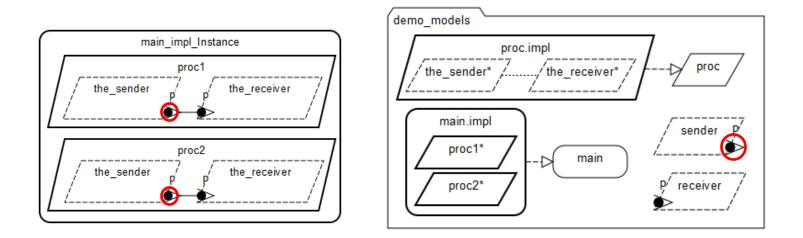
Example: Need for automated de-instantiation



Simple! Change the corresponding feature.



Example: Need for automated de-instantiation



But this also changes *p* in *proc2.the_sender* !!



Example: Need for automated de-instantiation

demo models refined

Solution with preservation of information is very

complicated with many extensions and refinements, even

package demo_models_refined
public
 system main
 end main;

system implementation main.impl subcomponents proc1: process proc.impl_ext; proc2: process proc.impl; end main.impl;

process proc
end proc;

process implementation proc.impl subcomponents the_sender: thread sender; the_receiver: thread receiver; connections cnx: feature the_sender.p -> the_receiver.p; end proc.impl; process implementation proc.impl_ext extends proc.impl subcomponents

the_sender: refined to thread sender_ext; end proc.impl_ext;

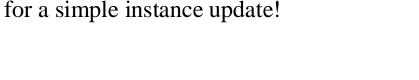
thread sender features p: out feature; end sender;

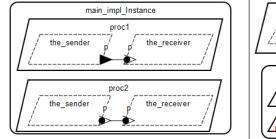
thread sender_ext extends sender
 features
 p: refined to out data port;
end sender_ext;

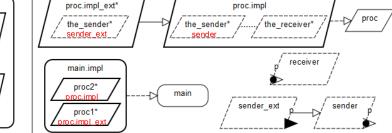
thread receiver features p: in feature; end receiver;

end demo_models_refined;

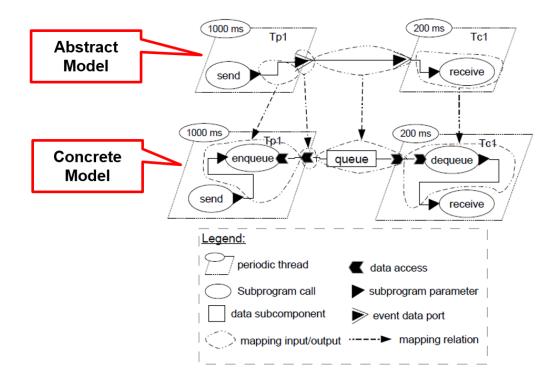








Real-life Scenario: RAMSES



Simplest refinement pattern in RAMSES:

Changing data port connection between two threads by replacing with a shared data component.

The data port features are changed to data access features.



Too many choices for de-instantiation in AADL! + Complications for information preservation due to many dependencies between elements and modularity.

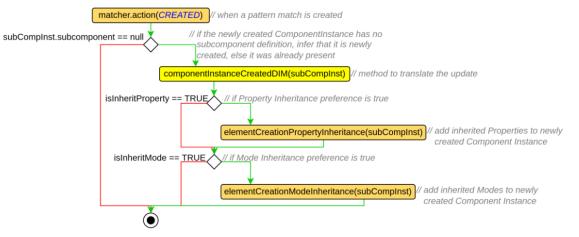
Makes de-instantiation of updates highly complex; requiring automation

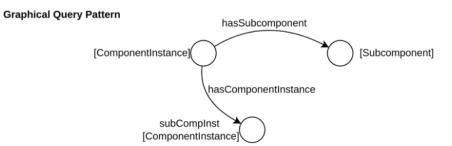
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OSATE-DIM

- OSATE Declarative-Instance Mapping
- Eclipse/OSATE-based plugin
- Graph Transformations
 - VIATRA
 - Graphical Queries
 - Model Transformation Rules







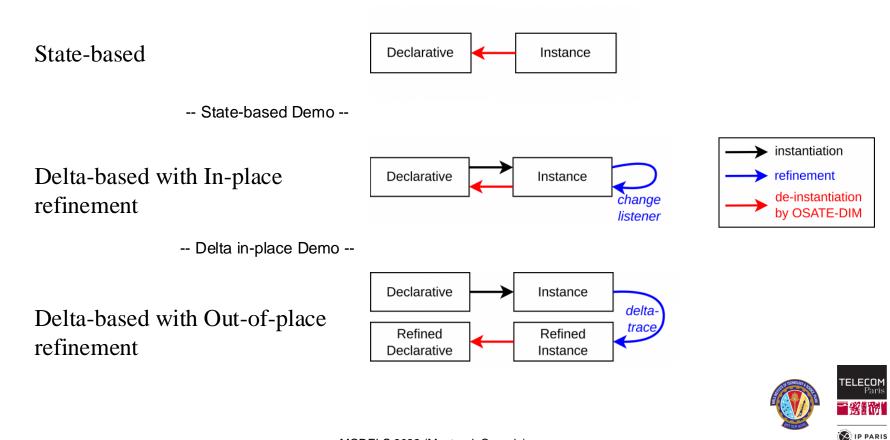


OSATE-DIM Values/Aims

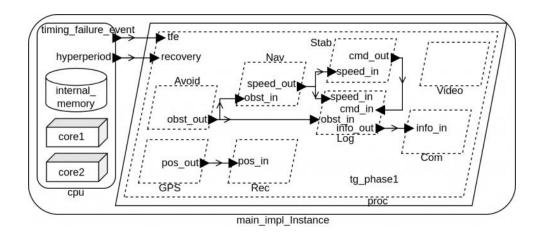
- Maximum Information Preservation
- Least/Minimal Change
- Very-well behaved lens (3 Lens laws)
 - No extraneous model updates.
 - Equality of updated-model state with updated view-state.
 - \circ Composability of updates.
- Flexibility
 - Scenarios
 - User preferences



Transformation Scenarios



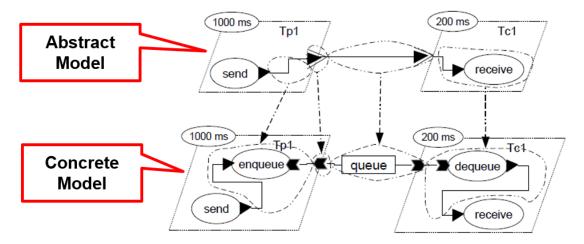
Case Study: MC-DAG



- Addition of *Property Associations* (RAMSES::Execution_Slots) for each *Thread*.
- Contain static scheduling tables for each *Thread* in different *Modes* LO and HI.
- Properties also reference the core and memory binding model elements, not just static data.



Case Study: RAMSES



- Addition of 1 (+40) *Data Components* to a *Process Component*, which are shared by two threads.
- The *Port Features* interfacing the two threads with each other are changed to *Data Access* kinds.
- New *Data Access Connections* are also added between the shared *Data Components* and the *Threads*.
- The added *Data Components* have varying numbers of *Properties*, and the total number of newly added properties is 122.



Conclusion

Introduced View-Update Problem in AADL-OSATE

OSATE-DIM is an automated solution for synchronizing Instance and Declarative models: 'de-instantiating' the Instance model

Three different scenarios

Wide range of view-updates supported

Tested on a preliminary test-bench

Simplifies the development of AADL model refinement tools



Future Work

Complete Implementation of Delta Out-of-place scenario

Further validation

Dissemination of OSATE-DIM to the AADL community

Concepts have potential to be used for "transpilation"



Thank you for your attention Questions?: Dominique



Tool Webpage: <u>mem4csd.telecom-paristech.fr</u> Zenodo artifact DOI: 10.5281/zenodo.6971720



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